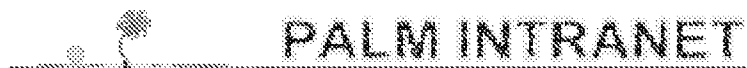


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Inventor Name Search Result

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10272670	6794247	150	10/16/2002	METHOD OF FABRICATING A SEMICONDUCTOR MEMORY DEVICE HAVING RESISTOR	KIM, BYUNG-SEO
10721533	7324613	150	11/25/2003	MULTI-LAYER DIFFERENTIAL PHASE SHIFT KEYING WITH BIT-INTERLEAVED CODED MODULATION AND OFDM	KIM, BYUNG-SEO
10737339	Not Issued	71	12/16/2003	Channel estimation and synchronization with preamble using polyphase code	KIM, BYUNG-SEO
10911157	7319254	150	08/02/2004	SEMICONDUCTOR MEMORY DEVICE HAVING RESISTOR AND METHOD OF FABRICATING THE SAME	KIM, BYUNG-SEO
11238381	Not Issued	71	09/29/2005	Semiconductor memory device with stacked control transistors	KIM, BYUNG-SEO
11783531	Not Issued	30	04/10/2007	Phase-changeable memory device and method of manufacturing the same	KIM, BYUNG-SEO

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